



IFC

**PATENT APPLICATION**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Satoshi SUGAHARA et al.

Attn: OIPE

Application No.: 10/550,652

Group Art Unit: 2811

Filed: December 7, 2005

Docket No.: 125426

For: RECONFIGURABLE LOGIC CIRCUIT USING A TRANSISTOR HAVING SPIN-DEPENDENT TRANSFER CHARACTERISTICS

**REQUEST FOR CORRECTION OF PALM RECORDS**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Attached is a photocopy of the original filing receipt on which errors have been corrected in red. These errors are being brought to the attention of the Patent and Trademark Office so that it may correct its records. A Supplemental Application Data Sheet is attached.

Respectfully submitted,

  
James A. Oliff  
Registration No. 27,075

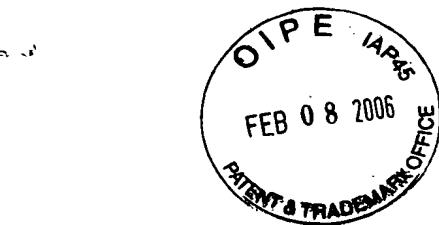
Randi B. Isaacs  
Registration No. 56,046

JAO:RBI/nxy

Date: February 8, 2006

OLIFF & BERRIDGE, PLC  
P.O. Box 19928  
Alexandria, Virginia 22320  
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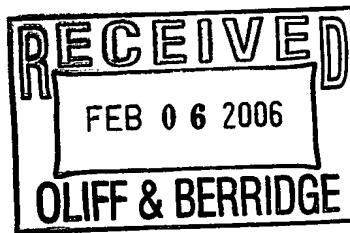


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APPL NO.	FILING OR 371 (c) DATE	ART UNIT	FIL FEE REC'D	ATTY/DOCKET NO	DRAWINGS	TOT CLMS	IND CLMS
10/550,652	12/07/2005	2811	2930	125426	40	45	5

25944  
 OLIFF & BERRIDGE, PLC  
 P.O. BOX 19928  
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FILING RECEIPT



\*OC000000017925835\*

Date Mailed: 02/02/2006

Receipt is acknowledged of this regular Patent Application. It will be considered in its order and you will be notified as to the results of the examination. Be sure to provide the U.S. APPLICATION NUMBER, FILING DATE, NAME OF APPLICANT, and TITLE OF INVENTION when inquiring about this application. Fees transmitted by check or draft are subject to collection. Please verify the accuracy of the data presented on this receipt. If an error is noted on this Filing Receipt, please mail to the Commissioner for Patents P.O. Box 1450 Alexandria Va 22313-1450. Please provide a copy of this Filing Receipt with the changes noted thereon. If you received a "Notice to File Missing Parts" for this application, please submit any corrections to this Filing Receipt with your reply to the Notice. When the USPTO processes the reply to the Notice, the USPTO will generate another Filing Receipt incorporating the requested corrections (if appropriate).

**Applicant(s)**

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 Tomohiro Matsuno, Tokyo, JAPAN;  
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**Assignment For Published Patent Application**

Japan Science and Technology Agency, Kawaguchi-shi, JAPAN

**Power of Attorney:** The patent practitioners associated with Customer Number 25944.

**Domestic Priority data as claimed by applicant**

This application is a 371 of PCT/JP04/04379 03/26/2004

**Foreign Applications**

JAPAN-2003086499 03/26/2003  
 2003-086499

**Projected Publication Date:** To Be Determined - pending completion of Security Review

**Non-Publication Request:** No

**Early Publication Request:** No

**Title** \* PLEASE SEE PAGE 2 FOR CORRECTIONS TO THE TITLE \*

logic  
Reconfigurable logical circuit using a transistor having spin-dependent transmission characteristics

**Preliminary Class**

257

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